VRW056 ṂṂ Ł QSQ ÆṀ ɥ s ǁ ˥ Silicon NPN transistor in a SOT-23 Plastic Package.

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Small reverse transfer capacitance, low noise figure.

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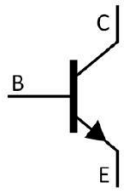
High frequency amplifier applications.

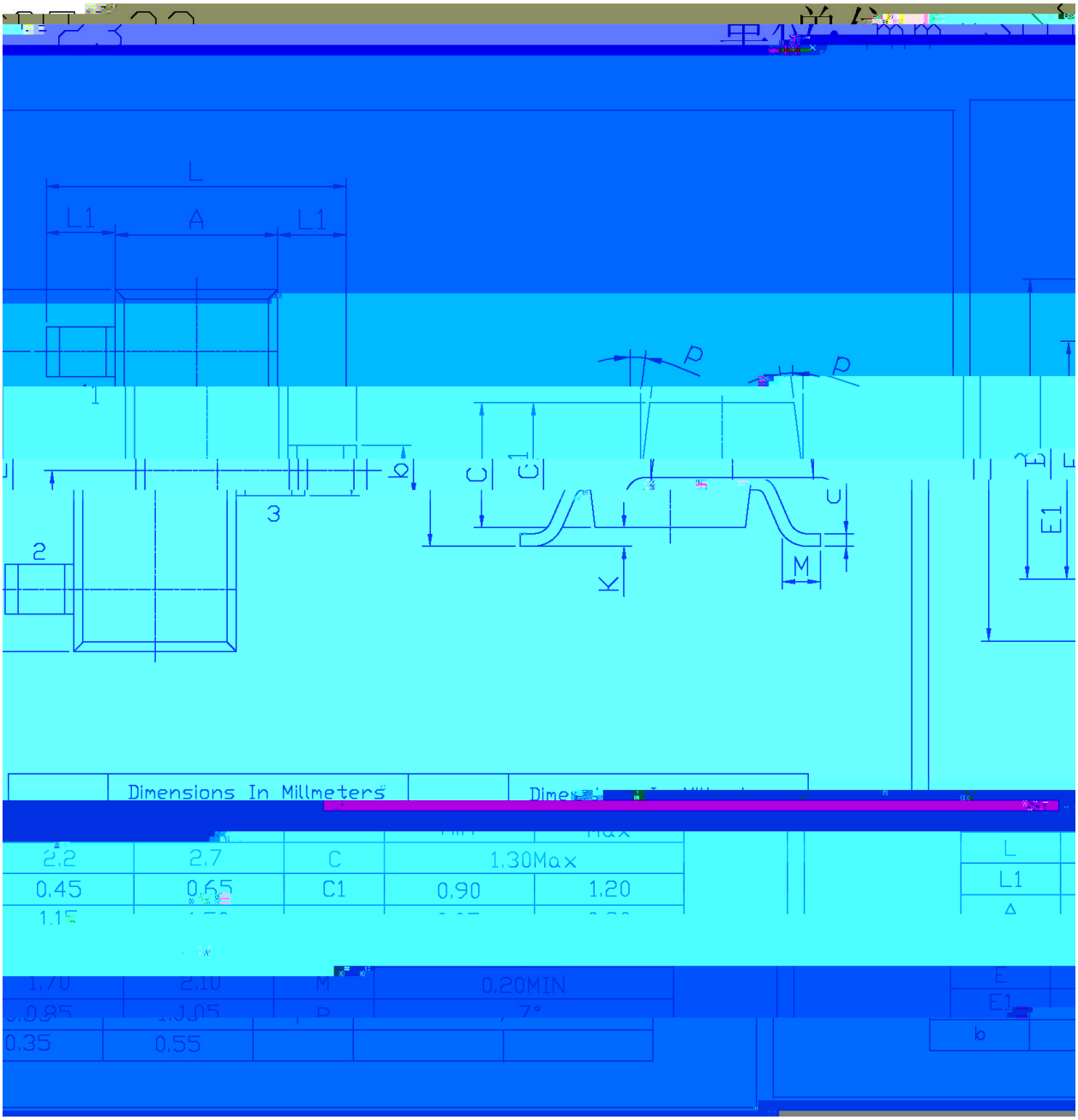




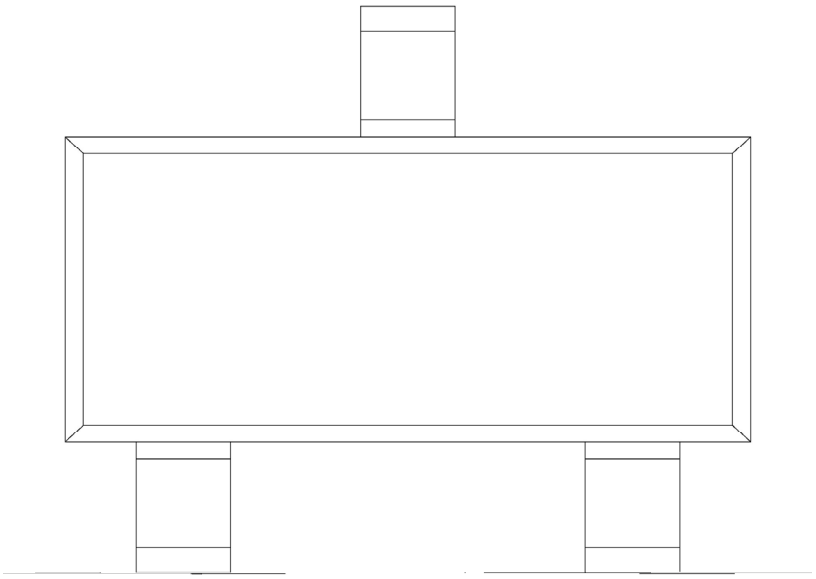
PIN1

v ̈Ω Parameter	ϟ N Symbol	̈Ω Ö Rating	D Ӈ Unit
Collector to Base Voltage	V_{CBO}	40	V
Collector to Emitter Voltage	V_{CEO}	30	V
Emitter to Base Voltage	V_{EBO}	4.0	V
Collector Current	I_C	20	mA
Emitter Current	I		

/ Package Dimensions



/ Marking Instructions



ب ˝ χ

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T χ ѭ ʒ ᴺ Ɱ

U χ ѭ h_{FE} ⁶ ₭ Ɱ

Note:

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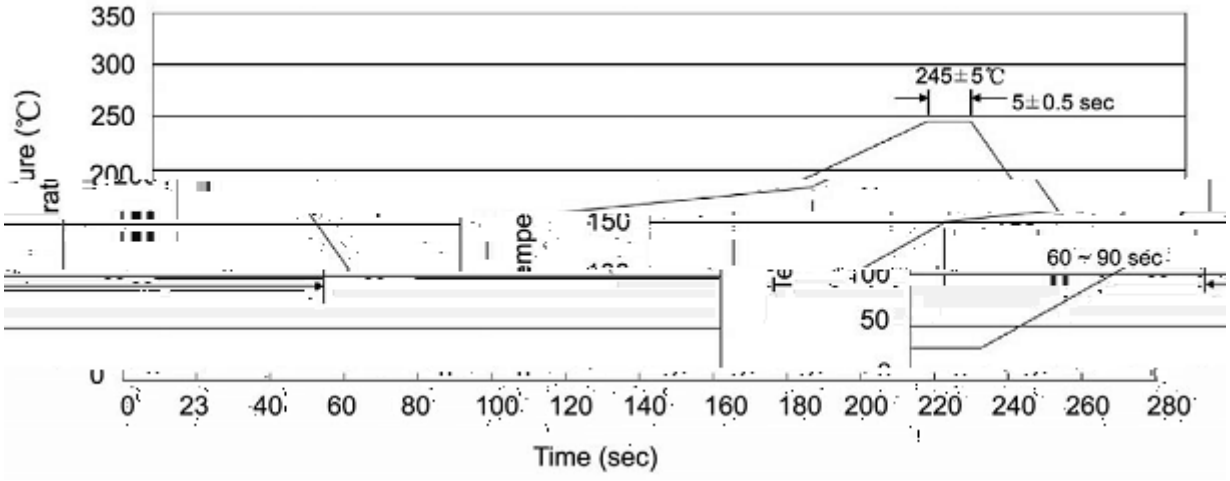
( ) / Temperature Profile for IR Reflow Soldering(Pb-Free)



ب ˝ χ

1 ℓ ♬ □ ỷ 25 Ḱ 150 ɬ ı `H ɮ 60 Ḱ 90sec;

2 Š Ö □ ỷ 245 ° 5 ɬ ı `H ɮ `Y ⺈ ⺮ 5 ° 0.5sec;

3 ì ˙ ˥ ˛ ɔ ɯ ỷ ⺮ 2 Ḱ 10 ɬ /sec.

Note:

1.Preheating:25~150 ɬ , Time:60~90sec.

2.Peak Temp.:245 ° 5 ɬ , Duration:5 ° 0.5sec.

3. Cooling Speed: 2~10 ɬ /sec.

/ Resistance to Soldering ⓑ Test Conditions

□ ỷ χ 260 ° 5 ɬ `H ɮ χ 10 ° 1 sec. Temp.:260±5 Time:10±1 sec

/ Packaging SPEC.

ɔ` γ ⊢ / REEL

Package Type 封装形式	Units 包装数量					Dimension 包装尺寸 (unit: mm^3)		
	Units/Reel 只/卷盘	Reels/Inner Box 卷盘/盒	Units/Inner Box 只/盒	Inner Boxes/Outer Box 盒/箱	Units/Outer Box 只/箱	Reel	Inner Box 盒	Outer Box 箱
SOT-23	3,000	10	30,000	6	180,000	7 ×8	180×120×180	390×385×205

/ Notices