BRCM24C64SC	SOP-8	64 Kbit	I²C	
	1.7V	2.5~5.5V	1Mhz	1.7~2.5V

Pin	Name	Type	Description
1	E0	Input	
2	E1	Input	
3	E2	Input	
4	GND	Ground	
5	SDA	I/O	/
6	SCL	Input	
7	WCB	Input	
8	VCC	Power	

			mA
ˆ ˜			V

		Ņ˘ ˘				

	≥ ≥	≤		

p

SDA SDA SCL (1) SCL
SDA (1)

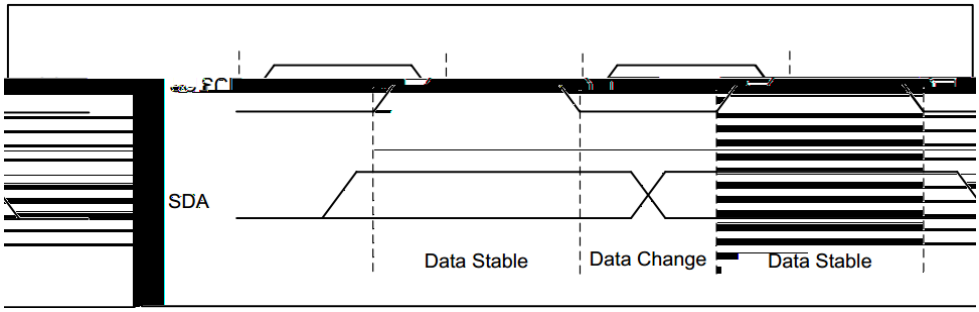


Figure 1 Data Validity

p

SCL SDA
2

.

p

SCL SDA
BRCM24C64SC

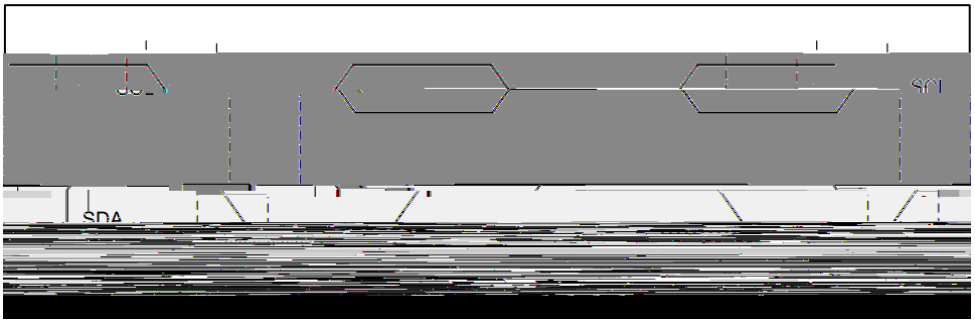
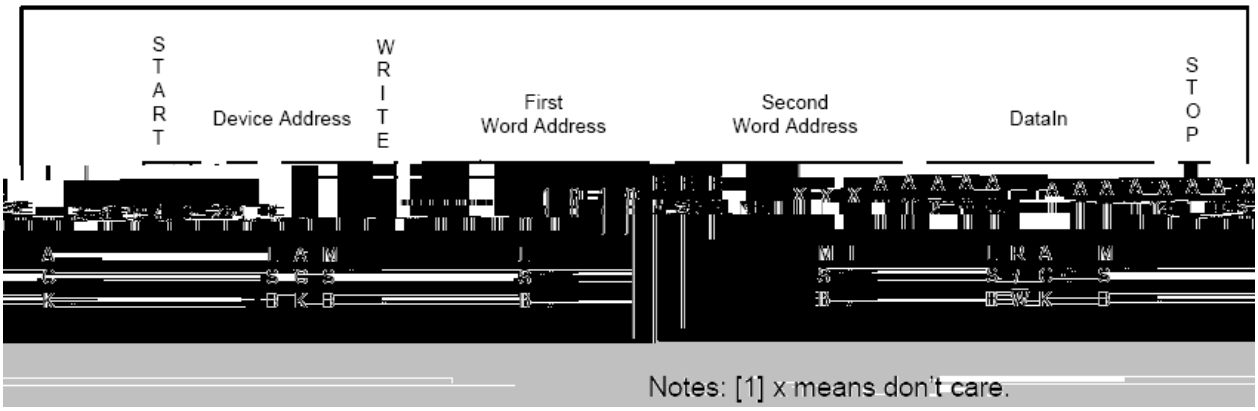


Figure 2 Start and Stop Definition

p

BRCM24C64SC BRCM24C64SC “ 0 ”
ACK 9



p

BRCM24C64SC BRCM24C64SC

“ 0 ”

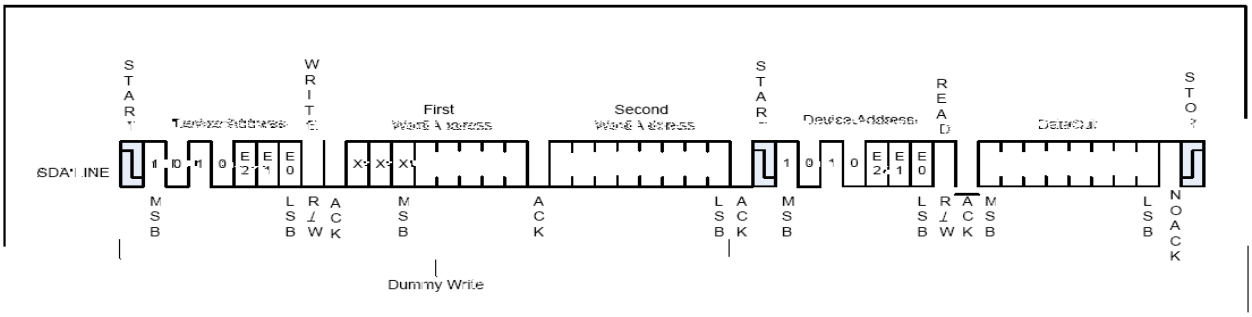


Figure 8 Random Address Read

p

BRCM24C64SC

“ 0 ”

9

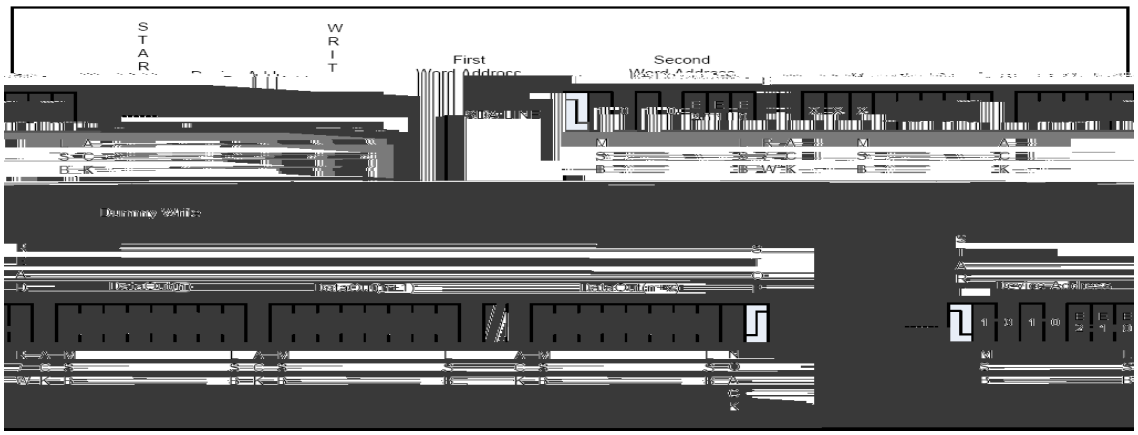


Figure 9

p

32

1011b A12/ A5 A4 / A0 ID

10d 22 ID

32

p

[+] /

ACK NoACK,(10)

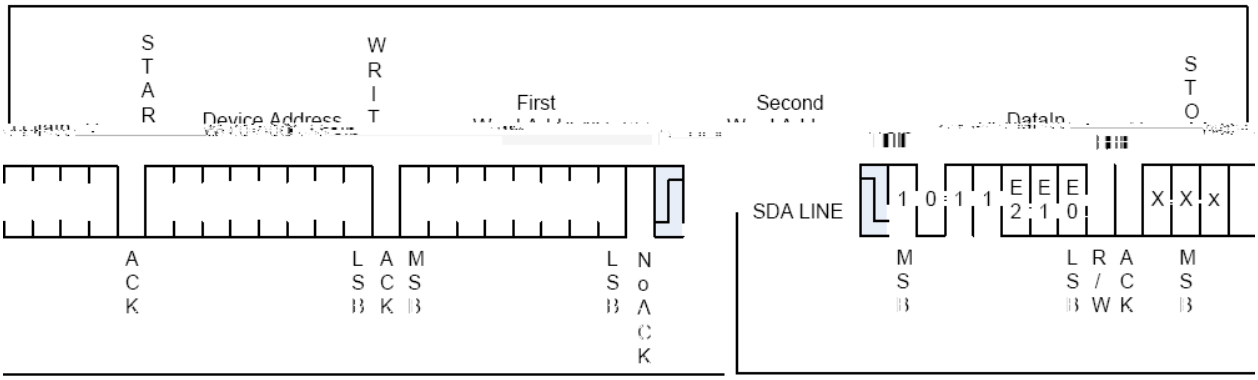


Figure 10 Lock Status Read (When Identification page locked, return NoACK after one data byte)

p

1

128

EEPROM “ ”

EEPROM

1

A11 A10 “ 10 ” 2 “ 10 ”

0800h

128 16

128 ACK

11

‘ ’

‘ ‘

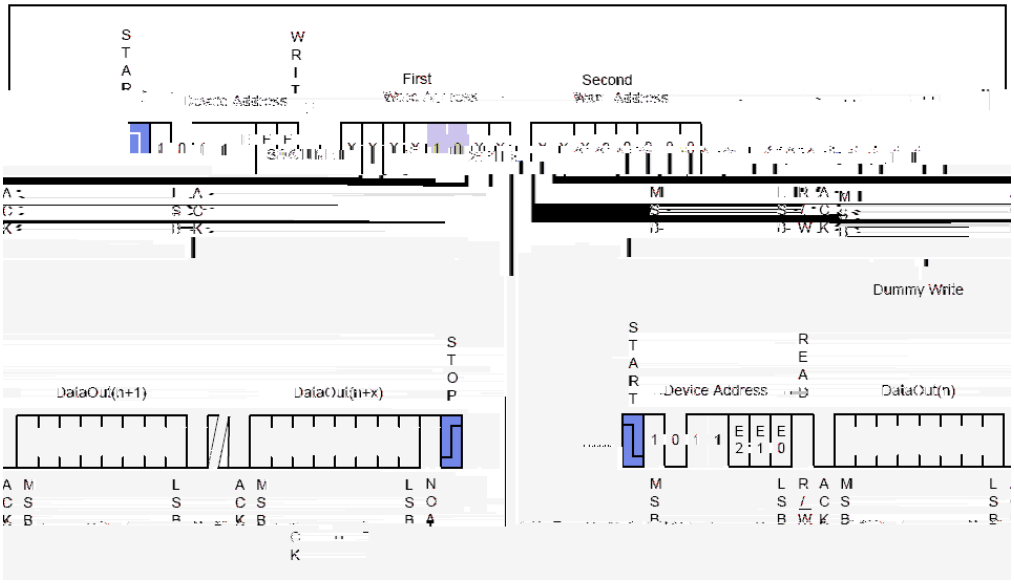
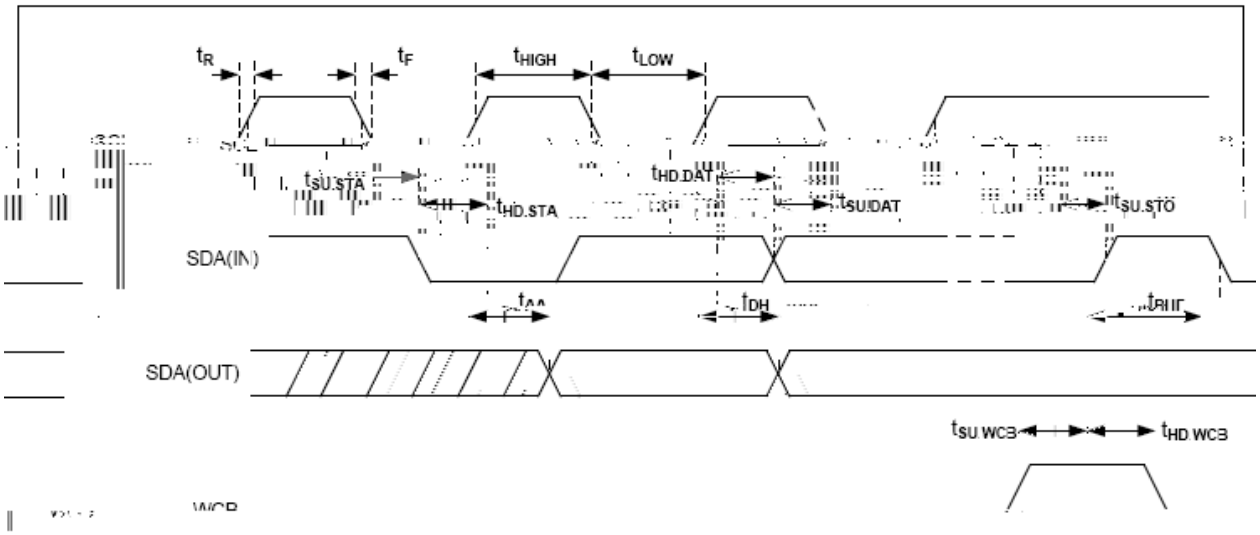
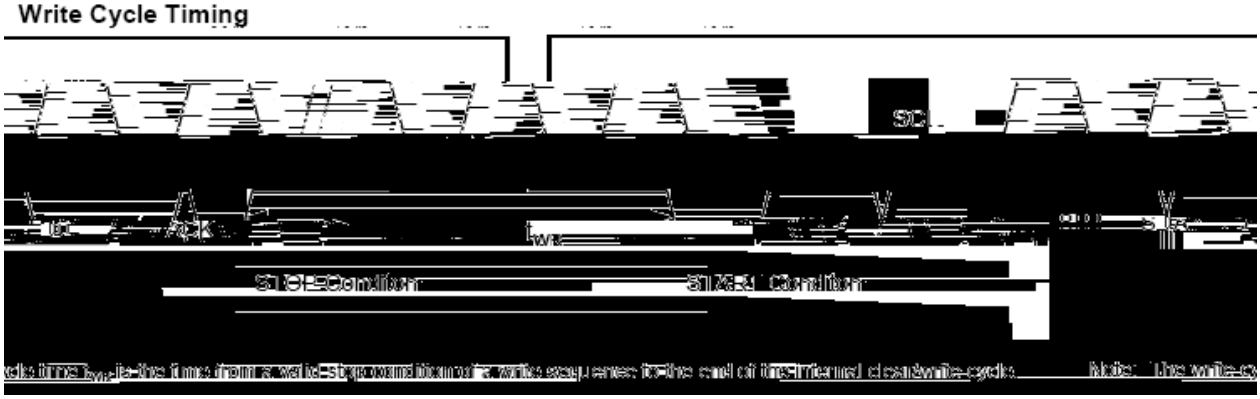


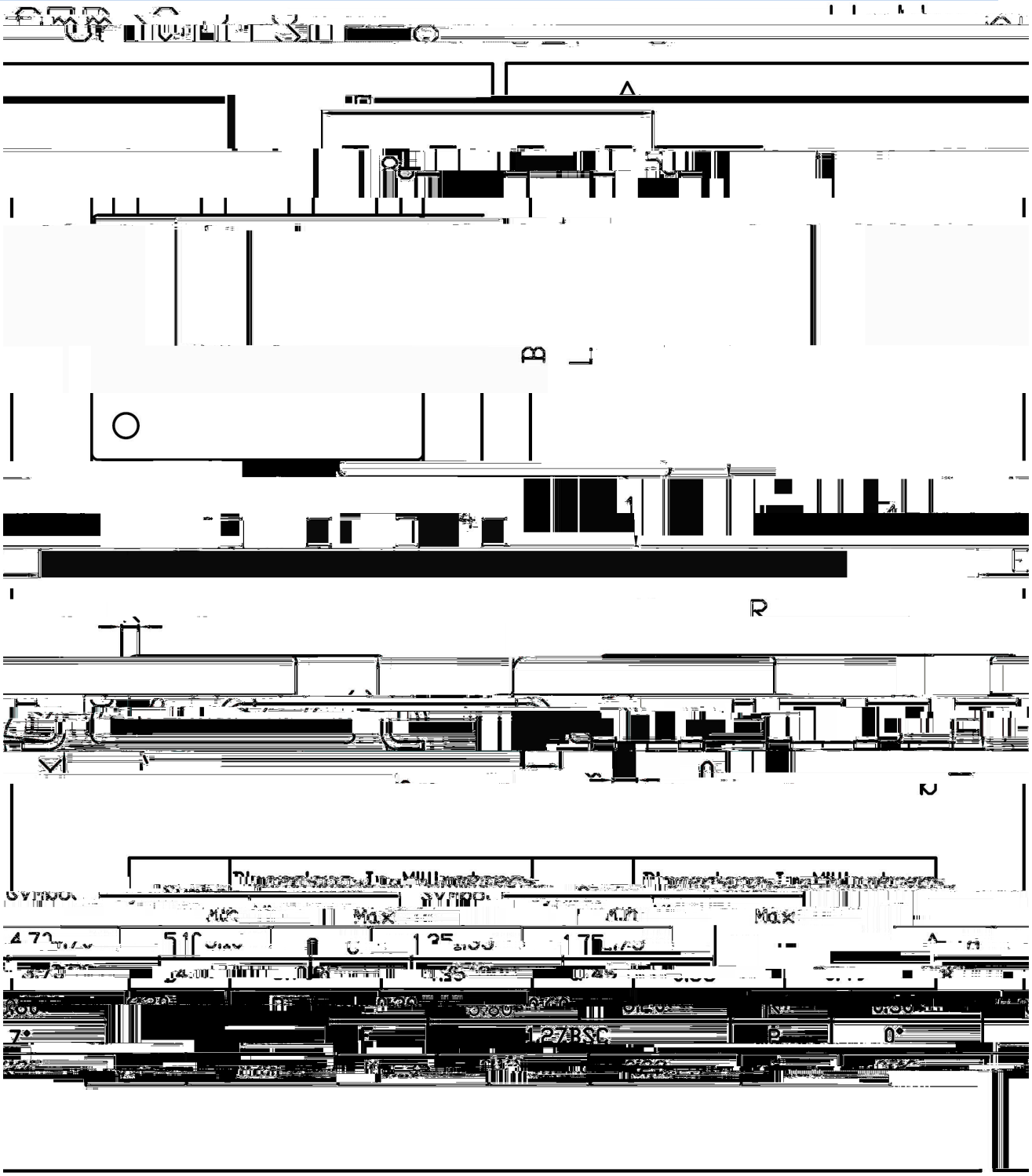
Figure 11 Sequential Read

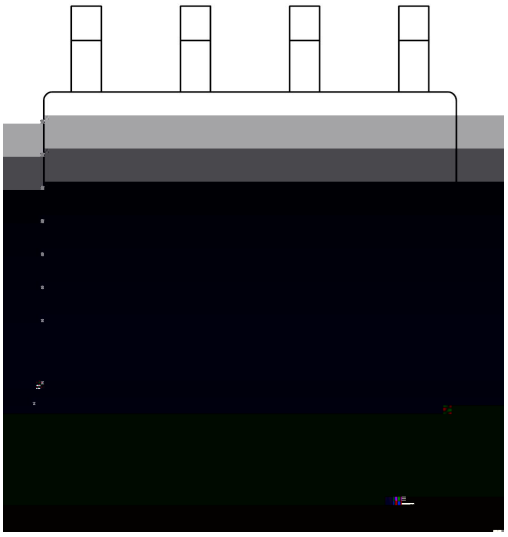
Bus Timing



Write Cycle Timing




Max
Max
1.27BSC



24C64

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